wherein the at least one processor is configured to re-process individual, failed segments after deactivating and reinitializing the individual, failed segment with corresponding data retrieved from the at least one memory circuit.

10. (New) The apparatus of claim 9 wherein at least one discrete event of the plurality of discrete events is a customer account and the at least one processor is configured to determine billing information for the customer account.

11. (New) The apparatus of claim 10 wherein the at least one processor is configured to generate an invoice for the customer account after processing the customer account.

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12. (New) The apparatus of claim 9 wherein at least one discrete event of the plurality of discrete events is a customer account and at least one independent sub-event of the discrete event comprises one or more customer calls.

13. (New) The apparatus of claim 9 wherein the at least one processor is included in one of a symmetrical multiprocessing system, a massively parallel processing system, and a loosely coupled distributed processing system.

14. (New) The apparatus of claim 9 wherein the number of segments is at least partially based on a number of processors included in the at least one processor.

15. (New) The apparatus of claim 9 wherein the size of individual segments is at least partially based on a number of customer accounts being processed.

16. (New) The apparatus of claim 15, wherein parent and child customer accounts are associated with the same segment and the size of the segment is partially based on the association.

17. (New) A method of processing a plurality of discrete events, individual discrete events of the plurality of discrete events comprising a plurality of independent sub-events, the method comprising:

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